| In re U.S. Patent Application of |) | |
|---------------------------------------|-----------------------|--|
| FUKUOKA et al. | { | |
| Application Number: 10/619,463 | Art Unit 2186 | |
| Filed: July 16, 2003 | { | |
| For: SEMICONDUCTOR INTEGRATED CIRCUIT | Examiner Iwashko, Lev | |
| Attorney Docket No. HITA.0408 | } | |

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE AND AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

This is in response to the Office Action mailed on January 18, 2006, the period of response to which is set to expire on June 18, 2006, with the payment of the appropriate government extension fee for two months. Please amend the above-referenced application as follows: